

## Application of Fuzzy Controller for Two-Leg Inverter Solar PV Grid Connected Systems with High Voltage Gain Boost Converter

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### Abstract

At present, the three-phase Voltage Source Inverters (VSI's) are playing a major role in Photovoltaic (PV) grid-connected systems. The drawbacks of conventional VSI's are high implementation complexity, high power loss and less reliable. In order to overcome the drawbacks of conventional VSI's, in this article, a fuzzy logic PWM controller-based two-leg three phase Voltage Source Inverter (VSI) is proposed to extract the maximum power of solar PV and to maintain constant grid voltage and frequency as it is having wide input and output operation and multiple fast controlling actions. The Low Pass Filter (LPF) based small-signal model slider Pulse Width Modulation (PWM) controller is used in Inductor Coupled Boost Converter (ICBC) to improve system dynamic response, voltage gain and infinite switching at different solar irradiation conditions. An adaptive Maximum Power Point Tracking (MPPT) technique is used to track the fast change of solar irradiances. The voltage at the DC-link capacitor and grid voltage are analyzed at different irradiation conditions.

**Keywords:** Duty cycle, fuzzy logic controller, SSICBC, small-signal model slider controller, two-leg three-phase inverter

### 1. Introduction

Out of all the renewable energy sources, solar PV (Photovoltaic) technology is one of the most powerful solutions to generate electricity from renewable energy sources. This technology has several advantages such as noise-free, less environmental pollution, no movable parts and easy implementation [1]. The drawback of solar power generation is installation cost. However, the installation cost of PV can be reduced by improving the efficiency of solar power generation and enhancing the manufacturing technology.

It has been noticed that over the past decade the PV power generation keeps on increasing. PV power is mainly used in batteries, fuel cells, hybrid electric vehicles, automotive systems, and headlamps, etc [2]. The interfacing of PV sources with the converter is one of the challenging tasks in PV power generation [3]. The PV power is used mostly in grid-connected systems and it is considered as a quick-rising section due to the lack of firewood power and ecological pollution [4].

From the literature survey [1-5], the per-unit electricity generation cost of PV power systems is high. In order to reduce the cost of solar power generation, a dc-dc converter is interfaced in between the PV system load. From the literature survey [6], there are different types of dc-dc converters are used to step up the PV voltage which is CBC, CIBC, and SEPIC etc. However, the drawbacks of conventional boost converters are less static voltage gain and high implementation complexity. In order to overcome the drawbacks of conventional boost converters, in this article, a single switch coupled inductor boost converter is used to step up the PV voltage. The voltage of PV mainly depends on the coupled inductor turns. The attractive features of the proposed boost converter are high voltage gain, continuous

output current and fewer ripples in converter output voltage [7].

The solar PV gives nonlinear I-V and P-V characteristics. As a result, it is difficult to transform the maximum power from the source to load. From the literature survey [8], there are different types of MPPT techniques are used to track the MPP. The most commonly used conventional MPPT techniques are: Perturb and Observe (P&O) [9], Hill Climb (HC) [10], and Incremental Conductance (IC) [11] which are used for low and medium power applications where the accuracy of MPP tracking is not needed. In P&O [9], the MPP tracking has been done by comparing instantaneous power ( $P_{\text{instant}}$ ) with the previous power ( $P_{\text{previous}}$ ). If the comparative results give the positive sign, it perturbs in the same direction otherwise perturbs in the negative direction. The major drawback of the P&O technique is high steady-state oscillation across MPP. In order to overcome this issue, an adaptive P&O algorithm is used in article [12] to reduce the oscillations across MPP. In addition, a PI controller is included in P&O to reduce the ripple content in PV voltage thereby improving the steady-state response of PV at different irradiation and temperature conditions. However, this method has the drawback of less MPP tracking efficiency.

In article [10], the HC MPPT technique is used to track MPP in which the variation of duty cycle with respect to the PV power ( $dP/dD$ ) is zero. If the change of power  $dP > 0$ , then the duty of boost converter is increased in order to achieve the  $dD > 0$ . If power  $dP < 0$ , then the duty of boost converter is reduced in order to achieve the  $dD < 0$ . In this way, the duty cycle of the boost converter is adjusted by comparing the instantaneous power with the previous power. The major drawback of the HC method is less accuracy in MPP tracking at the rapid change of atmospheric conditions.

The drawbacks of the HC technique can be confronted by using the IC MPPT technique [11]. This technique operates based on the slope of I-V and P-V characteristics of the solar

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PV. The slope of the P-V curve is zero when the operating point of PV reaches the MPP. The slope of the P-V curve is positive when the operating point of PV is on the left side of MPP otherwise it is negative. The disadvantage of the IC MPPT technique is less MPP tracking speed at dynamic irradiation conditions of solar PV. In order to overcome the drawbacks of conventional MPPT techniques, in this article, an adaptive MPPT technique is used to track the MPP with high speed.

From the literature survey [13], there are different types of PWM techniques are used in PV fed grid-connected systems to generate the switching pulses to the inverter. The Sine PWM technique is used in article [14] in order to reduce the switching power losses and maintaining the constant grid voltage. The SVPWM is used in article [15] to reduce the ripple content in the output voltage with a high modulation index. From the proper selection of switching angles, the lower order harmonics of the inverter output voltage is eliminated by using a Neural Network based PWM technique [16]. The drawback of the NN technique is required for high training time. In order to overcome the drawbacks of the above PWM techniques, in this article, a fuzzy logic PWM controller-based PWM technique is used in two-leg VSI to maintain the load voltage constant with unity power factor.

## 2. Proposed system description

In this article, a two-stage power conversion interface between solar PV and grid and it is used in low, medium and high-power applications is shown in Fig.1. In this circuit, the PV panel consists of multiple arrays that are connected in series. This series connection gives a high voltage for PV grid-connected applications [2]. The circuit in Fig.1 is designed with two converter steps. The first step is non-isolated ICBC for high voltage gain with increased performance and the second level is a two-leg three-phase inverter for converting DC to AC. Here, the power decoupling between the dc-dc converter and inverter has been stretched by using the coupling capacitors  $C_{d1}$  and  $C_{d2}$ . In addition, these capacitors are used to store electrical energy. The system load in each individual phase, by absolute current generation as  $I_{pa}$ ,  $I_{pb}$ , and  $I_{pc}$ . Grid side inductors ( $L_g$ ) and resistors ( $R_g$ ) are used to indicate the three-phase transformer and that transformer is interfaced in between the inverter and grid. It also provides an enhanced improvement in grid power quality. In order to achieve higher efficiency from the solar PV, it is essential to maintain the balance between PV array and load, so that the operational point of PV coincides with the MPP [17].

In this work, an adaptive MPPT method technique is considered for improving the system stability and to harvest the maximum power of solar PV. This technique gives better dynamic behavior of the system and also improves the phase shifting of the system signal. This technique is used to track the fast change of solar irradiances. The major focus is on an efficient LPF based small-signal model slider PWM controller for SSICBC and a fuzzy logic-based PWM controller for a two-leg three-phase inverter.

The small-signal model slider PWM technique is used when the system is in dynamic operation and it is required for an infinite switching sliding control [1]. The slider control is one of the most useful approaches and flexible PWM controller for the operation of higher-order nonlinear systems during unreliable operating conditions. The slider control in Fig.1 is used to stabilize the solar panel output voltage. The

purpose of this controller is to eliminate voltage ripples across PV terminals. In the second stage, the main objective is to add a fuzzy PWM controller for inverter operation. This method possesses a lot of advantages such as less implementation cost, controlling an extensive range of operations, less mathematical modeling, reducing THD of inverter output voltage and a very fast system controlling action.

The midpoint of the dc-link capacitors ( $C_{d1}$ - $C_{d2}$ ) acts as a neutral point. The fuzzy controller is used to balance the neutral point from different uncertainties (different sun irradiation and temperature conditions). The proper design of two-leg inverter gives the load power with unity power factor, sinusoidal currents, constant grid voltage, and less THD. The three-phase output voltages and currents of the inverter are controlled by the use of open delta connection of load across the grid.

The article is structured as follows. The "design of a single diode PV equivalent circuit" is explained in Section-III. Section-IV gives the control and design of slider controller-based ICBC. In Section-V, the design of two leg inverter is explained. In Section-VI, MATLAB-simulation results are presented and finally, conclusions are presented in Section-VII.

## 3. Design of solar photovoltaic module

In this PV module, the solar cells are designed by using monocrystalline silicon material. The combination of cells forms a module and these modules are combined to form an array. The series connection of arrays leads to the generation of a string and all these strings combine together to form the PV panel. Based on the type of PV module used, the real power output of PV is measured as watt-peak (Wp). The PV panel shows nonlinear characteristics of P-V and I-V [18]. The solar power generation depends mainly on solar module operating temperature, sun irradiances and voltages. The basic single diode model of PV-cell is shown in Fig.2.

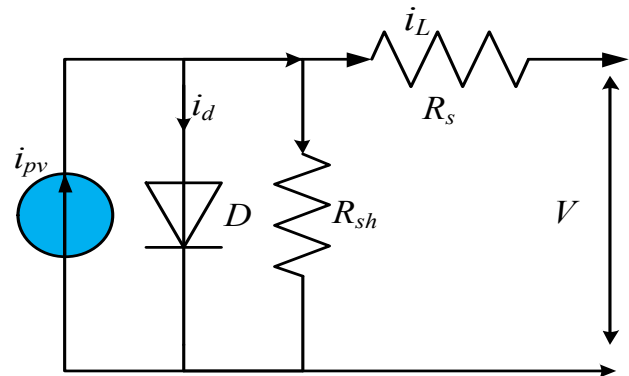


Fig.2. Mathematical model of single PV module

The basic factors considered to design a solar PV are irradiances  $1000\text{W/m}^2$ , air mass of 1.5 and solar panel junction temperature  $25^\circ\text{C}$  at benchmark test conditions determine the nominal power of solar PV [19]. For designing a solar panel, a basic single diode model solar PV is considered with five general parameters expressed as, solar cell current ( $i_{pv}$ ), diode diffusion current ( $i_0$ ), air mass ( $a$ ), series resistance ( $R_s$ ) and parallel resistance ( $R_{sh}$ ). The PV output current is given as,

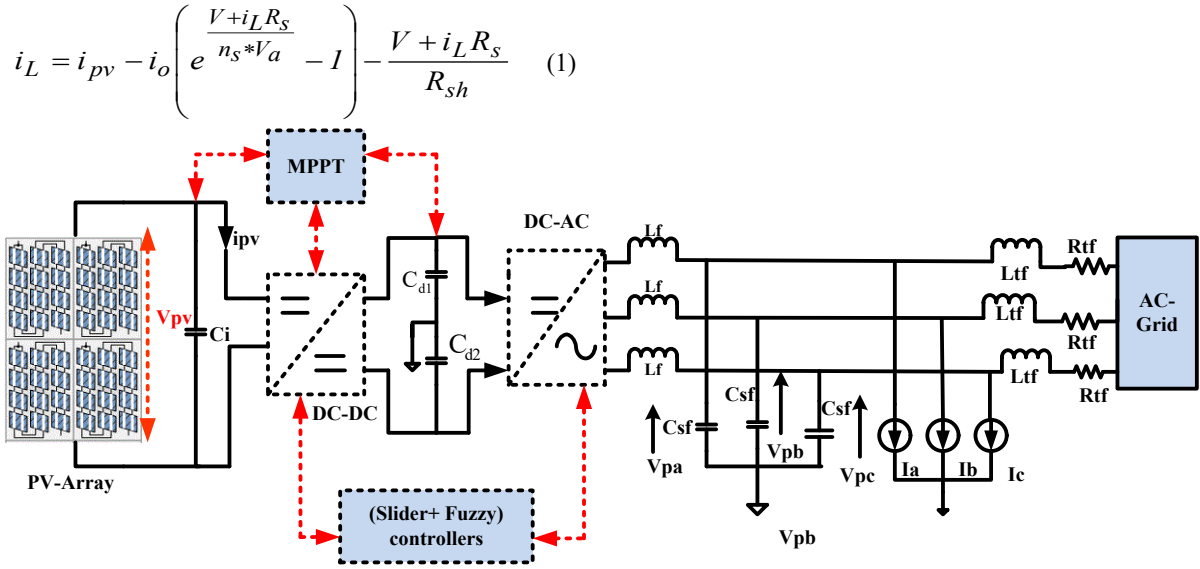


Fig. 1. Two stages of power conversion: (a). ICBC DC-DC converter. (b). Two- legs three-phase inverter (basic operation) [1]

Where,

$$V_a = \frac{Akt_{stc}}{q} \quad (2)$$

Where  $V_a$  is the thermal voltage,  $t_{stc}$  is the junction temperature at STC,  $n_s$  is the number of series cells and  $A$  is the diode quality factor. The solar panel design parameters are given in Table.1. Based on Eq.1, the short circuit current ( $i_{sc}$ ), MPPT current ( $I_{MPPT}$ ) and open circuit current ( $i_{oc}$ ) are derived as [1],

$$i_{sc} = i_{pv} - i_0 * \left( e^{\frac{i_{sc}R_s}{n_s * V_a}} - 1 \right) - \frac{i_{sc}R_s}{R_{sh}} \quad (3)$$

$$I_{MPPT} = i_{pv} - i_0 * \left( e^{\frac{V_{MPPT} + I_{MPPT}R_s}{n_s * V_a}} - 1 \right) - \frac{V_{MPPT} + I_{MPPT}R_s}{R_{sh}} \quad (4)$$

$$i_{oc} = 0 = i_{pv} - i_0 \left( e^{\frac{V_{oc}}{n_s * V_a}} - 1 \right) - \frac{V_{oc}}{R_{sh}} \quad (5)$$

From the P-V characteristics of the PV panel, at the maximum power point, the differentiation of PV power with respect to the PV voltage is zero.

$$\left( \frac{dp_{pv}}{dv} \right)_{V=V_{MPPT}} = 0 \quad (6)$$

Based on I-V characteristics, the short-circuited shunt resistance is calculated as shown in Eq.7.

$$\left( \frac{dI}{dv} \right)_{I=i_{sc}} = -\frac{1}{R_{sho}} \quad (7)$$

Where  $R_{sho}$  is the output short-circuited shunt resistance. From  $i_{sc}$  and  $V_{oc}$ , the PV panel current is calculated as,

$$i_{pv} = i_0 \left( e^{\frac{V_{oc}}{n_s * V_a}} \right) + \frac{V_{oc}}{R_{sh}} \quad (8)$$

By substituting Eq.8 in 3, the short circuit current is calculated as,

$$i_{sc} = i_0 \left( e^{\frac{V_{oc}}{n_s * V_a}} - e^{\frac{i_{sc}R_s}{n_s * V_a}} \right) + \frac{V_{oc} - i_{sc}R_s}{R_{sh}} \quad (9)$$

From Eq.9, the approximated short circuit current is expressed as in Eq.10,

$$i_{sc} = i_0 \left( e^{\frac{V_{oc}}{n_s * V_a}} \right) + \frac{V_{oc} - i_{sc}R_s}{R_{sh}} \quad (10)$$

Based on the above equations, diode saturation current is derived in terms of  $V_{oc}$  and  $i_{sc}$ .

$$i_0 = \left( i_{sc} - \frac{V_{oc} - i_{sc} * R_s}{R_{sh}} \right) * e^{\left( \frac{-V_{oc}}{n_s * V_a} \right)} \quad (11)$$

By substituting equations (11) and (8) in (4), the maximum peak current is expressed as.

$$I_{MPPT} = i_{sc} - I_x - I_y * e^{I_z} \quad (12)$$

Where,

$$I_x = \frac{V_{MPPT} + I_{MPPT}R_s - i_{sc}R_s}{R_{sh}} \quad (13)$$

$$I_y = i_{sc} - \frac{V_{oc} - i_{sc}R_s}{R_{sh}} \quad (14)$$

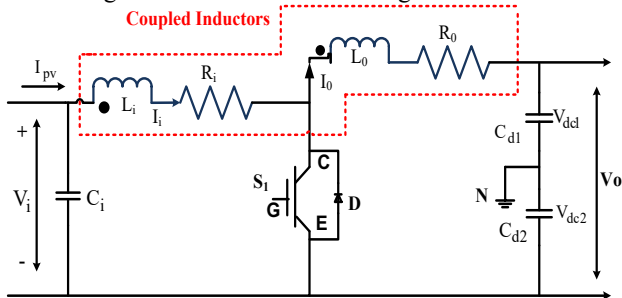
$$I_z = \frac{V_{MPPT} + I_{MPPT}R_s - V_{oc}}{n_s * V_a} \quad (15)$$

**Table 1.** Solar panel data and the designed parameters

Parameters	symbol	Value	S.I. Units
<b><i>Panel data at the standard test condition</i></b>			
• Panel temperature	$T$	25	$^{\circ}\text{C}$
• Air mass	$A$	1.5	
• Irradiance	$I_i$	500	$\text{W/m}^2$
<b><i>Designed parameters</i></b>			
• Maximum peak power	$P_{MPPT}$	5.18178	kW
• Open circuit voltage	$V_{oc}$	497.38	V
• Maximum peak voltage	$V_{MPPT}$	402.00	V
• Temperature coefficient of PV-panel at $V_{oc}$	$K_v$	-0.355	%per $^{\circ}\text{C}$
• No of parallel strings	$N_p$	2	-
• No of series-connected cells per string	$n_s$	24	-
• Short circuit current	$i_{sc}$	14.05	A
• Maximum peak current	$I_{MPPT}$	12.89	A
• Temperature coefficient at short circuit current	$K_i$	0.06	%per $^{\circ}\text{C}$
• Solar panel current	$i_L$	8.8455	A
• Diode diffusion current	$i_0$	$2.27 \times 10^{-10}$	A
• Diode ideality factor		0.97625	
• Shunt connected resistance	$R_{Sh}$	86.8053	Ohm
• Series resistance	$R_S$	0.27328	Ohm

#### 4. Operation of capacitor clamp inductor coupled boost converter

Basically, a conventional DC-DC boost converter is used for low, medium voltage and high-power applications. The circuit diagram of ICBC is shown in Fig.3.



**Fig. 3.** Capacitor clamp inductor coupled boost converter

This converter gives better steady-state and dynamic behavior as compared to other converters with a very low voltage stress on the switch. Moreover, it is used for improving the voltage gain as compared to other non-isolated converters for PV grid-connected applications [1]. The switch 'S<sub>1</sub>' is the Insulated Gate Bipolar Transistor (IGBT) which is generally used for high power applications and a body diode is used to control reverse recovery voltage across the switch. The main features of this converter are less electromagnetic interference, more flexibility, enhanced efficiency, and reduced switching power losses. The voltage gain is derived from input and output loops of the DC-DC converter. The voltage gain of the boost converter is as follows,

$$\frac{V_0}{V_i} = \frac{I + N_{tr}D}{D} \quad (16)$$

Where  $N_{tr}=N_2/N_1$  is the coupled inductors turns ratio.  $N_1$  and  $N_2$  are the primary and secondary inductors winding turns respectively.

The coupled inductor technique is one of the upcoming technique to step up the PV voltage for many applications, such as signal processing, central processing unit. In this article, a boost converter is used for improving the voltage gain by the use of coupled inductor and it gives good steady-state and transient performance [1]. Coupled inductor minimizes the circuit structure and optimizes the faster transient response for the medium and high voltage of PV grid-connected applications [20].

The primary, secondary and total inductors are derived as,

$$L_i = \frac{L_t}{(1 + N^2)} \quad (17)$$

$$L_0 = \frac{N^2}{I + N^2} * L_t = N^2 L_i \quad (18)$$

Where,  $L_b$ ,  $L_o$  and  $L_t$  are the primary, secondary and total inductance respectively. The primary, secondary and total winding resistances  $R_b$ ,  $R_o$  and  $R_t$  are given as,

$$R_i = \frac{R_t}{I + N} \quad (19)$$

$$R_0 = \left( \frac{N}{I+N} \right) * R_t \quad (20)$$

The design parameters of ICBC and VSI are mentioned in Table 2.

**Table 2.** Converter and inverter design parameters

Parameters	Values
$R_l$	23.85 mΩ
$C_{d2}$	10.00 mF
$C_i$	10.00 mF
$L_2$	14.04 mH
$C_{d1}$	10.00 mF
$L_1$	15.60 mH
$R_2$	71.25 mΩ
$R_{ff}$	0.267 Ω
$L_{ff}$	12.00 mH
$C_{\beta}$	0.600 mF
$C_{sf}$	0.500mF

#### 4.1 Small signal model slider controller

In conventional DC-DC converter operation, sensing of the source signals for all state parameters is not mandatory for the generation of PWM pulse. Basically, error signals are enough to control the DC-DC converter and are generated by high pass filtering of the state parameters. Furthermore, a direct current component of these state parameters is altered by converter input and output voltage signals balance condition [1], [7]. The main drawback of this high pass filter-based slider controller is that it increases the system order, which effects the dynamic behavior of the system. The proposed small-signal LPF based slider controller is applied to control the ICBC. The main intention of this technique is to control the nonlinear behavior, wide input and output voltage, load imbalance, maintaining stability constant at any operating condition and improving system dynamic response. The LPF is reduced the THD of the converter output voltage.

The initial step of designing SSICBC is to select the state variables and the indirect measurement of magnetic flux which is shown in the below equation,

$$I_a = \begin{cases} I_i & t \in T_{on} \\ I_i * (I+N) & t \in T_{off} \end{cases} \quad (21)$$

Where, the switch "ON" time  $T_{on} = DT$ , switch "OFF" time  $T_{off} = (1-D) T$ , the total operating time of switch  $T = T_{on} + T_{off}$  and  $N$  is the winding turns ratio. In equation (21),  $I_i$  and  $I_a$  are the primary and magnetizing inductor currents. By differentiating the above equation (21), the differentiation of magnetizing and output voltages are as follows,

$$\frac{dI_a}{dt} = \frac{V_i - i_a R_a}{L_i} + \left[ \left( \frac{V_i - V_o}{L_i(I+N)} * (I-u) \right) \right] - I_k \quad (22)$$

$$I_k = \left[ \left( \frac{R_i + R_o}{L_i * (I+N)^2} * i_a \right) * (I-u) \right] \quad (23)$$

$$\frac{dV_o}{dt} = \left[ \left( \frac{i_a}{(I+N) * C_i} \right) * (I-u) \right] - \frac{i_o}{C_i} \quad (24)$$

Where,  $u \in \{0,1\}$

If  $u = 1$ , IGBT is ON  
= 0, IGBT is OFF

The vector "X" of state parameters errors is mentioned as follows,

$$X = [x_1, x_2] = [i_a - I_a^{ref}, v_o - V_o^{ref}] \quad (25)$$

The mathematical standard design equation is as follows,

$$\dot{X} = aX + bu + az + f \quad (26)$$

Where,

$$a = \begin{bmatrix} -\frac{(R_i + R_o)}{L_i * (I+N)^2} & \frac{-1}{L_i * (I+N)} \\ \frac{1}{C_i * (I+N)} & 0 \end{bmatrix}, f = \begin{bmatrix} \frac{V_i}{L_i * (I+N)} \\ \frac{-i_o}{C_i} \end{bmatrix} \quad (27)$$

$$b = \frac{V_i}{L_i} - \frac{R_i * i_a}{L_i} - \left( \frac{V_i - V_o}{L_i * (I+N)} \right) + V_e + V_f \quad (28)$$

$$V_e = \frac{R_i + R_o}{L_i * (I+N)^2} * i_a \quad (29)$$

$$V_f = \frac{i_a}{C * (I+N)} \quad (30)$$

The reference value of the duty cycle for SSICBC is calculated as,

$$D = \left( \frac{I - \frac{V_i}{V_o}}{1 + \frac{NV_i}{V_o}} \right) \quad (31)$$

Based on the variable structure theory, a sliding face is needed to consider within the state parameter region, where the control parameters are discontinuous. The sliding surface is given by equation (32),

$$S(x) = \beta_1 x_1 + \beta_2 x_2 = \beta^T X : \text{where, } \beta^T = [\beta_1 \quad \beta_2] \quad (32)$$

From the above condition, the sliding surface requires the state trajectories nearer the surface directed towards the sliding surfaces and the mathematical surface conditions are calculated as,

$$\begin{cases} \dot{S}(x) < 0 & \text{if } S(x) > 0 \\ \dot{S}(x) > 0 & \text{if } S(x) < 0 \end{cases} \quad (33)$$

In order to conclude, the circuit state surface closes by the sliding face, a particular switch action is required to design a control law to move forward. The system condition to run in the sliding mode in infinite time is given by,

$$u = \begin{cases} 0 & \text{if } S(x) > 0 \\ 1 & \text{if } S(x) < 0 \end{cases} \quad (34)$$

From Eq's.25, 32 and 23, the existing conditions are written as,

$$\lambda_1(X) = \beta^T aX + \beta^T G < 0, \text{ if } S(x) > 0 \quad (35)$$

$$\lambda_2(X) = \beta^T aX + \beta^T b + \beta^T G < 0, \text{ if } S(x) < 0 \quad (36)$$

Where,

$$G = az + f$$

Where  $\lambda_1(x)$  &  $\lambda_2(x)$  are two lines in the phase plane.

The equation (34) is satisfied with the sliding surface  $S(x) = 0$ , hence the state trajectories start from a central point on the sliding region and it converges to the sliding surface. The state trajectories once reach the sliding surface; the existence condition includes the origin point. Hence the system operates with high stability. Basically, the slider is operating at an infinite switching frequency condition. Hence the state trajectories direction is towards the slider surface and it moves along the sliding surface. In case the slider is operating at a finite switching frequency, it necessary to apply a control law for the sliding surface. The control law is taken from [1].

$$u = \begin{cases} 0 & \text{if } S(x) > \Delta \\ 1 & \text{if } S(x) < \Delta \end{cases} \quad (37)$$

Where  $\Delta$  an arbitrary positive value. The design parameters of the slider controller are selected to track the MPPT point at different irradiation conditions (1000, 750 and 500W/m<sup>2</sup>) and it is mentioned in Table.3.

#### 4.2 Adaptive MPPT algorithm

For a successful controller operation, it is necessary to run solar PV at MPPT as described in Section-II. The main problems in solar power generation are non-linear I-V and P-V characteristics. To overcome this non-linear behavior, an adaptive MPPT technique is used [21]. This technique is widely applied for the fast-tracking of solar irradianations. The MPPT controller technique is shown in Fig.4 and it consists of low pass filter with cut off frequency  $\omega_L$ , a demodulator  $\phi_d = 1$ , an integrator gain ( $K_i = 5.4$ ) and a lead compensator  $A(s)$ . In this controller, an adaptive MPPT and LPF with lead compensator is considered.

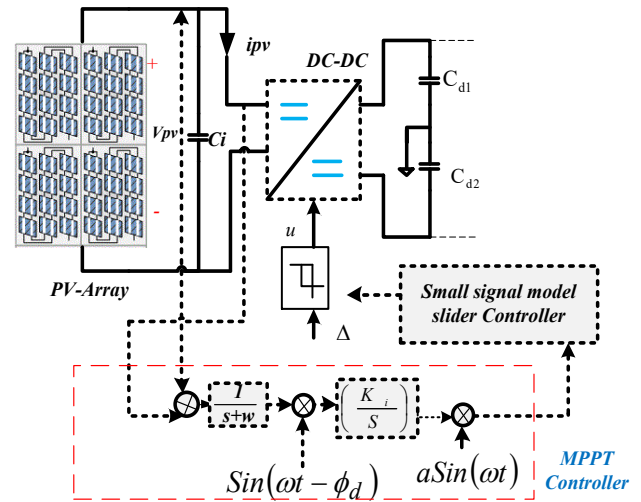
The combination of LPF and the integrator is an ultimate solution to eliminate high-frequency noise component from multidimensional signals that are associated with providing the gradient value [1]. Finally, the resultant signal is added with a small signal sine wave to determine the MPPT point. The main aim of the MPPT technique is to get the maximum power in the PV-DC link to change the P-V and I-V characteristics of the solar module.

The MPPT controller reference voltage  $V_{MPPTref}$  is fed to a slider controller to give an adaptive sliding surface, which is added by a slider controller to a constant value of voltage in order to attain the MPPT. The small signal model slider

controller takes current and voltage errors as inputs for regulating the solar voltage and currents. Based on solar radiation, the sliding surface is varied step by step using the adaptive MPPT technique.

**Table 3.** Slider controller design parameters

Parameters	Values
$C(s)$	1.000
$K_i$	5.400
$A$	0.200
$\beta_1$	5.00
$\beta_2$	0.10
$\omega_h$	10.0 rad/sec
$\omega$	100.0 rad/sec
$\Delta$	1.50



**Fig. 4.** Small signal model slider controller

#### 5. Design of TWO-LEG INVERTER

The design description of two leg inverter topology is illustrated in Section-II. From the literature survey [22], the conventional three-leg VSI converts dc-ac and it gives supply voltage to the grid. In the worst case, if anyone of the leg fails in the conventional inverter, the entire supply may get shut down. In order to overcome this issue, a two-leg inverter topology is used for the continuous power supply to the grid. From Fig.5, phase A supplies the line to line voltage of  $E_a$  and phase B supplies the line to line voltage of  $E_b$ . The RMS value of line to line voltages ( $E_a, E_b$ ) is equal to  $E$ . The inverter gives the phase voltages of  $V_{inv-a}, V_{inv-b}$  and its corresponding phase currents are  $I_{inv-a}$  and  $I_{inv-b}$ . The filter ( $L_f-C_f$ ) is used to suppress the inverter output voltage and current ripples. From the article [1], the authors proposed the slider controller to generate the switching pulses to the inverter and it requires many sensors to sense the different state variables and references for each pulse generation and hence, it is more complex to control grid voltage and power factor. So, the slider controller is not popular for inverter operation. To overcome this a fuzzy logic PWM controller for two-leg VSI is proposed as shown in Fig.5. The mathematical modeling of the two-leg inverter [1] is as follows,



$$\frac{dI_j}{dt} = \frac{2}{3L_{tf}} (E_j - 2R_{tf}I_j - V_{pj} - I_k R_{tf}) - I_v \quad (38)$$

$$\frac{dV_{cd1}}{dt} = \frac{-1}{C_{d1}} (u_a I_{inv-a} + u_b I_{inv-b}) + \frac{I_{mag}(1-u)}{C_{d1}(1+N)} \quad (41)$$

$$I_v = \frac{1}{3L_{tf}} (E_k - 2R_{tf}I_k - V_{pk} - I_j R_{tf}) \quad (39)$$

$$\frac{dV_{cd2}}{dt} = \frac{1}{C_{d2}} ((1-u_a)I_{inv-a} + (1-u_b)I_{inv-b}) + \frac{I_{mag}(1-u)}{C_{d2}(1+N)} \quad (42)$$

$$\frac{dI_{invj}}{dt} = \frac{1}{C_{sf}} (I_j + I_{invj} - I_{pj}) \quad (40)$$

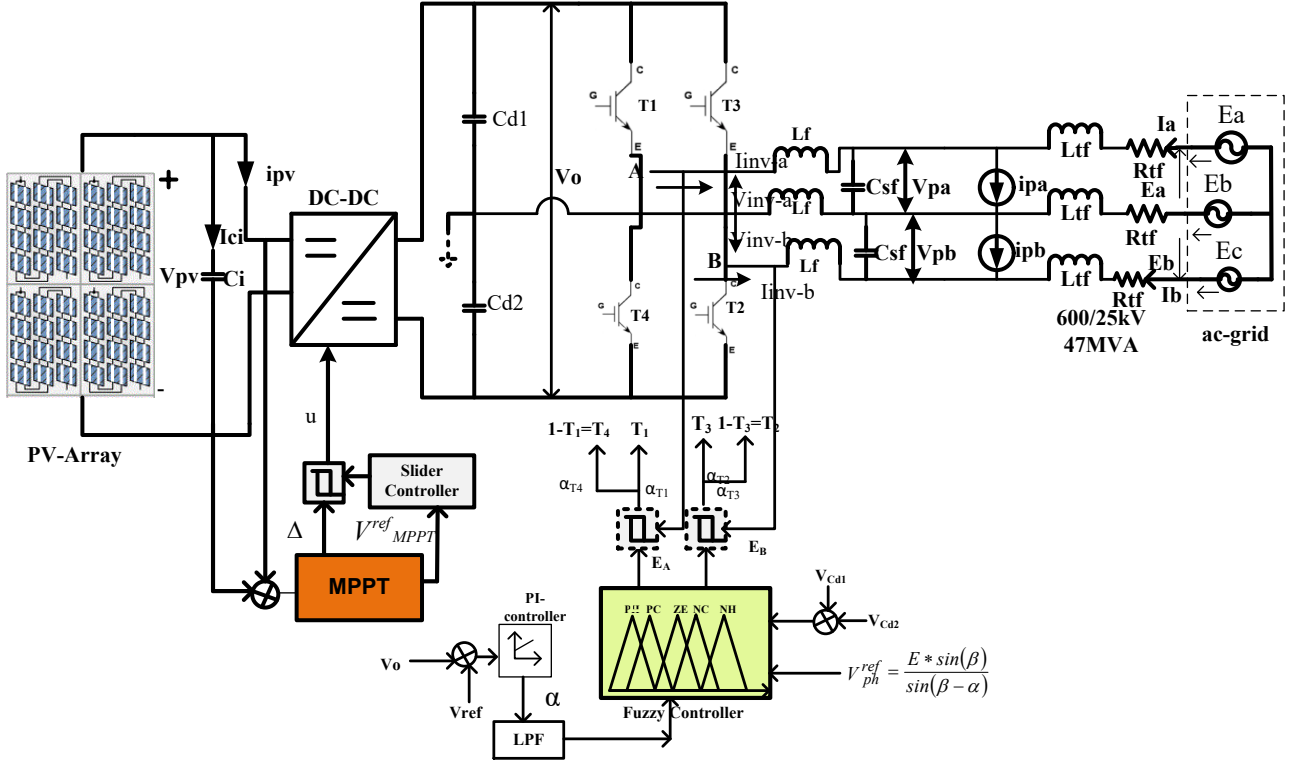


Fig. 5. FLC PWM controller-based two-leg three-phase inverter

Where the subscript indicates  $j$ ,  $K \in \{a, b\}$ . The inverter dc-link voltages are given in equations (37) and (41). The principle operation of two-leg VSI is shown in Table.4. Here, when the switches  $T_1$  and  $T_2$  are in ON condition, then the voltage across the phase A and B are  $V_{inv,a}=V_0/2$ ,  $V_{inv,b}=-V_0/2$  and  $V_{inv,c}=0$ . Similarly, when the switches  $T_3$  and  $T_4$  are in ON condition, the voltage across phase A and B are  $V_{inv,a}=-V_0/2$ ,  $V_{inv,b}=V_0/2$  and  $V_{inv,c}=0$ . The switching angle of IGBT's  $T_1$  and  $T_2$  are  $\alpha_{T1}$  and  $\alpha_{T2}$ . Similarly, the switching angle of IGBT's  $T_3$  and  $T_4$  are  $\alpha_{T3}$  and  $\alpha_{T4}$ . The open delta connected phase voltages and phase currents of the load are expressed as  $v_{pa}$ ,  $v_{pb}$ ,  $i_{pa}$ , and  $i_{pb}$ .

In the fuzzy logic PWM controller, three operations are involved: fuzzification, inference mechanism, and defuzzification. The processes of converting input variables to linguistic functions are called fuzzification [23], [24]. The inference process controls the behavior of the surface related to the input and output variables and is defined by a set of rules which are given in Table.5. The final process, defuzzification, transforms the linguistic functions into crisp solutions before using it for a two-leg three-phase inverter. The main objective of fuzzy is to transfer maximum dc-dc converter output voltage through a two-leg inverter to the grid [9]. The major problem in solar power generation is the

variation of the grid terminal voltage magnitude, which can be eliminated by the defuzzification technique.

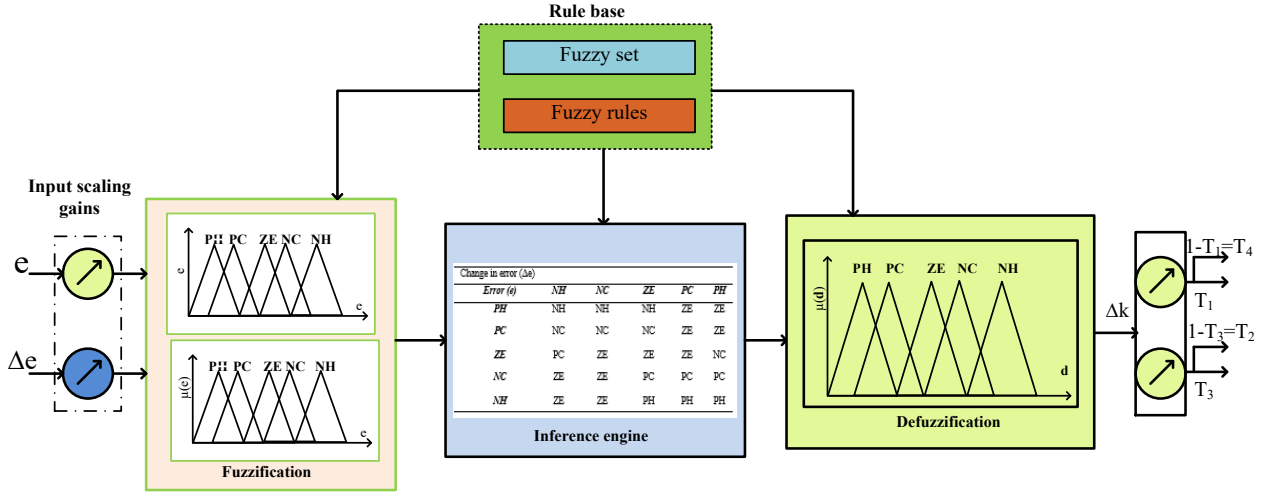
The fuzzy rules are designed by using the error and change in error of converter dc-link voltage ( $V_0$ ), grid RMS voltage ( $V_{ref}$ ) and difference between the capacitor voltages ( $V_{dc1}$ ,  $V_{dc2}$ ). The maximum power is achieved by varying the modulation index of the DC-AC inverter based on input voltage and output voltage. These variables are fuzzified and are mentioned in the form of linguistic variables expressed by their membership functions which are predefined for every set. The membership function of the firing angle generation is given in Fig.6. The major advantage of the fuzzy logic PWM controller is that it doesn't consider mathematical equations and its process purely depends on a certain number of rules. The rules are expressed in five levels, namely Positive High (PH), Negative High (NH), Positive Compact (PC), Negative Compact (NC) and Zero (ZE). The implementation of the fuzzy logic PWM controller is easy as compared to the slider PWM controller and it gives better performance over the years. The PWM pulses for the two-leg inverter is given in Fig.7. The contribution of applying the fuzzy PWM technique is to obtain balanced load voltage with high stability. In addition, it is used to get the unity power factor of load and grid. By applying a displacement angle ' $\alpha$ ', the maximum power of solar PV is transferred to the grid and local loads. The dc-link

voltage ( $V_0$ ) along with a PI controller is applied to the PWM controller in order to get the unity power factor. The

displacement angle is derived as,  $V_{ph}$

**Table 4.** Switching operation of two-leg three-phase inverter

Nodes		Switching condition				Inverter o/p voltage		
A	B	$T_1$	$T_2$	$T_3$	$T_4$	$V_{inv a}$	$V_{inv b}$	$V_{inv c}$
0	0	0	1	0	1	$-V_0/3$	$-V_0/3$	$2V_0/3$
0	1	0	0	1	1	$-V_0$	$V_0$	0
1	0	1	1	0	0	$V_0$	$-V_0$	0
1	1	1	0	1	0	$V_0/3$	$V_0/3$	$-2V_0/3$



**Fig.6.** Fuzzy logic controller for two legs three-phase inverter

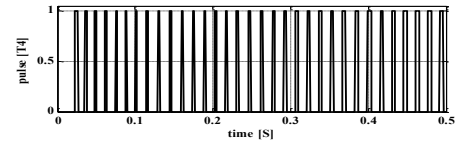
**Table 5.** Fuzzy rules for two-leg inverter PWM generation

Change in error ( $\Delta e$ )					
Error (e)	NH	NC	ZE	PC	PH
PH	NH	NH	NH	ZE	ZE
PC	NC	NC	NC	ZE	ZE
ZE	PC	ZE	ZE	ZE	NC
NC	ZE	ZE	PC	PC	PC
NH	ZE	ZE	PH	PH	PH

$$\alpha = K_P (V_0^{ref} - V_0) + K_i \int (V_0^{ref} - V_0) dt \quad (43)$$

At grid side, the line to line RMS voltage is derived as,

$$V_{ph}^{ref} = \frac{E \sin(\beta)}{\sin(\beta - \alpha)} \quad (44)$$



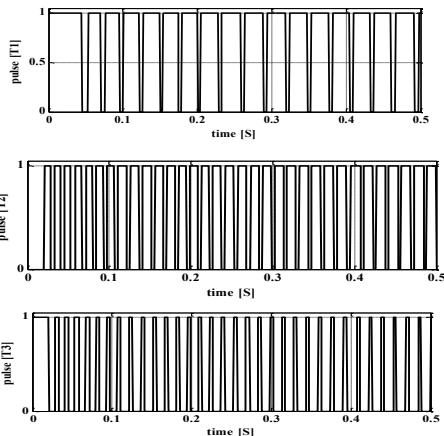
**Fig.7.** Switching pulses for two leg inverter

## 5. Simulation results and discussion

The nonlinear I-V and P-V characteristics of the solar panel (designed based on Table.1) are shown in Fig.8.

These characteristic curves are obtained with a single diode PV-module that is explained in Section-III at different irradiances 1000 W/m<sup>2</sup>, 750 W/m<sup>2</sup> and 500 W/m<sup>2</sup> respectively. From Fig.8 (a) and (b), the maximum power point is indicated at different irradiation levels. At 1000 W/m<sup>2</sup>, the PV peak current and voltages are 23.33A and 415V. Similarly, at 750W/m<sup>2</sup> and 500W/m<sup>2</sup>, the PV peak currents and voltages are 16.82A, 406V and 12.89A, 402V respectively. The entire two-stage power conversion system is simulated and its performance is evaluated and demonstrated with different irradiation conditions as in Fig.9.

From Fig.9 at 1000 W/m<sup>2</sup>, the PV power increases linearly up to 14ms and after that, it is constant up to 4s. At





750 W/m<sup>2</sup> and 500 W/m<sup>2</sup>, the step change in PV power occurs at 4.2 and 6.3msec.

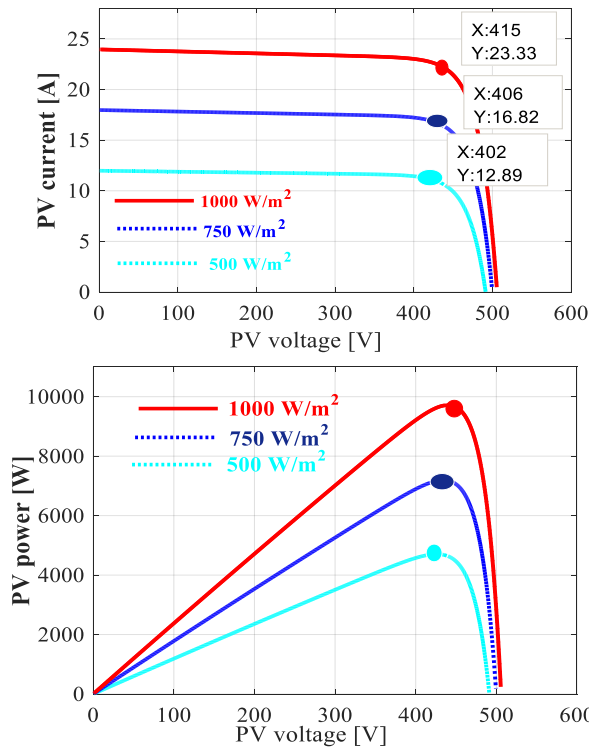


Fig. 8. (a) I-V curves at different irradiation levels (1000, 750 and 500W/m<sup>2</sup>), (b) correlated P-V curves

Based on PV power variations, the adaptive slider controller tracks MPP with continuous variation of solar radiation. This MPPT technique gives a better dynamic performance with less oscillations and offers a better efficiency of around 97.8%. Because of the input capacitor (C<sub>i</sub>) of the converter, the coupled inductor DC-DC converter steady-state output voltage ripples are negligible. The simulated DC-DC converter output voltage and its zoom view at 1000 W/m<sup>2</sup> is given in Fig.10.

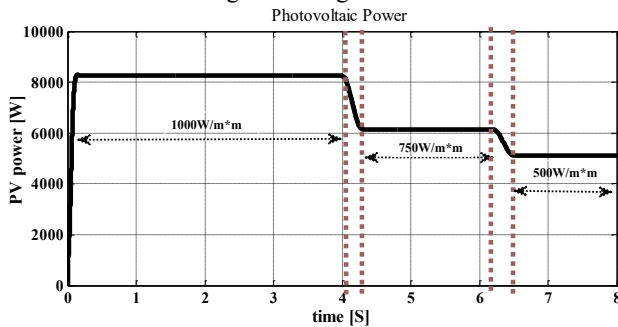


Fig. 9. PV power at variable irradiation condition (1000 W/m<sup>2</sup>, 750 W/m<sup>2</sup> and 500 W/m<sup>2</sup>)

The DC-link capacitors are used to clamp the coupled inductor voltage ripples of the boost converter. The voltages of the DC-link capacitors are shown in Fig.11 and the corresponding zoom view gives the steady-state voltages at 1000W/m<sup>2</sup>. The inverter output of the L<sub>r</sub>-C<sub>f</sub> filter is used to reduce THD of three-phase grid currents of the inverter which is shown in Fig.12 and the zoom view gives steady-state grid currents at 1000 W/m<sup>2</sup>.

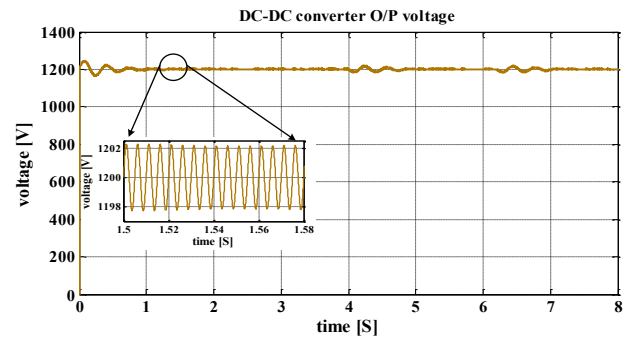


Fig. 10. DC-DC converter output voltage at different irradiances, 1000 W/m<sup>2</sup>, 750 W/m<sup>2</sup> and 500 W/m<sup>2</sup>, (including zoom view at 1000 W/m<sup>2</sup>)

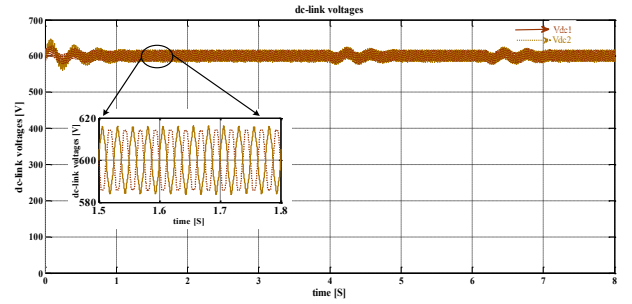


Fig. 11. DC-link capacitor voltages at different irradiation levels, 1000 W/m<sup>2</sup>, 750 W/m<sup>2</sup> and 500 W/m<sup>2</sup>, (including zoom view at 1000 W/m<sup>2</sup>)

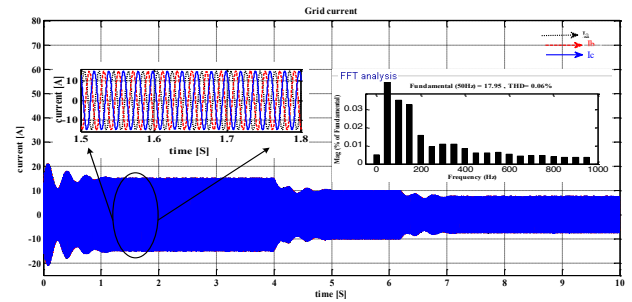


Fig. 12. Three-phase grid currents at different irradiation levels, 1000 W/m<sup>2</sup>, 750 W/m<sup>2</sup> and 500 W/m<sup>2</sup>, (including zoom view at 1000 W/m<sup>2</sup>)

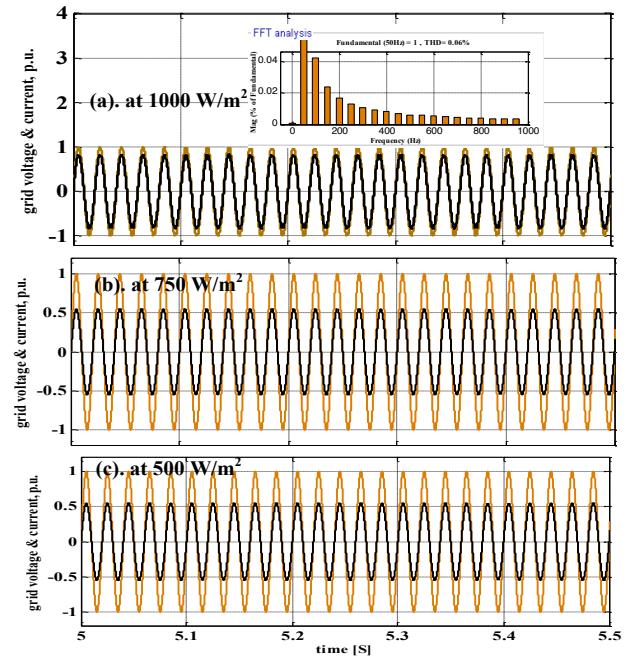


Fig. 13. Three-phase load (Grid) voltage and currents at different irradiation conditions, 1000, 750 and 500 W/m<sup>2</sup>

The performance of slider and fuzzy logic PWM control techniques is evaluated by considering the parameters such as efficiency, ICBC slider surface, the variation of modulation index of the inverter, THD and maximum power extraction. At 1000W/m<sup>2</sup>, the base and real value of the currents are 20A and 16A. Similarly, the base and real value of voltage are 600V and 580V. The simulated per unit grid current and voltages are shown in Fig.13 (a), (b) and (c) at different irradiation levels. In addition, the THD of grid voltage is included in Fig.13. From the results of three-phase voltages and currents, the system is operating with a unity power factor with zero voltage and current ripples. From all the simulated results and mathematical calculations, it is observed that the proposed solar power generation system is well suited to industrial as well as grid-connected applications.

## 7. Conclusion

The detailed proposed strategy is implemented in two stages. In stage one, a small signal model LPF based slider controller is implemented to improve the SSICBC voltage gain, maintaining the voltage balance between the PV and converter output at infinite switching control. In the second

stage, a two-leg VSI used, in which the utilization of switches is reduced by two driver circuits to trigger the switches. Hence, in the inverter switching, conduction losses and size of the inverter reduces compare to conventional six switch inverter. The fuzzy logic PWM controller is used in the two-leg inverter to reduce system design complexity, load voltage imbalance and to maintain load at unity power factor. An adaptive MPPT is used to track the maximum power of the PV panel. Therefore, it is concluded that the fuzzy logic based two-leg three-phase inverter for solar PV-grid connected application will operate effectively and efficiently with high accuracy.

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